

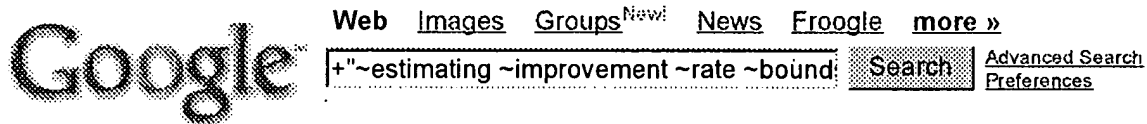
Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	78	706/19.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:47
L2	4773791	problem\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L3	1123216	complex\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L4	10218	L2 adj3 L3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L5	5202596	detect\$4 determin\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L6	323	L5 with L4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L7	225	706/14.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L8	305	706/15.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L9	509	L7 xor L8 L7 and L8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54

L10	229	706/12.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L11	236	706/13.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L12	449	L10 xor L11 L10 and L11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L13	913	L9 xor L12 L9 and L12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L14	978	L13 xor L1 L13 and L1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L15	41	706/902.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L16	1015	L14 xor L15 L14 and L15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:54
L17	4	L6 and L16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:58
L18	3	17 and @ad<="20010604"	US-PGPUB; USPAT; USOCR	OR	ON	2005/01/27 10:15
L19	2248127	estimat\$4 approximat\$4	US-PGPUB; USPAT; USOCR	OR	ON	2005/01/27 09:58
L20	4958593	bound\$4 rang\$3 limit\$6 threshold\$3	US-PGPUB; USPAT; USOCR	OR	ON	2005/01/27 09:58

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L22	243501	rat\$4 adj3 L21	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:58
L23	430	L19 with L20 with L22	US-PGPUB; USPAT; USOCR	OR	ON	2005/01/27 10:07
L24	0	L23 and L16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 10:14
L25	3584	L19 same L20 same L22	US-PGPUB; USPAT; USOCR	OR	ON	2005/01/27 10:08
L26	4	L25 and L16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 10:14
L27	3	26 and @ad<="20010604"	US-PGPUB; USPAT; USOCR	OR	ON	2005/01/27 10:15

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L1	0	706/19.ccls.	DERWENT	OR	ON	2005/01/27 09:20
L2	78	706/19.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:21
L3	225	706/14.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:20
L4	305	706/15.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:20
L5	509	L3 xor L4 L3 and L4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:20
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L7	236	706/13.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:20
L8	449	L6 xor L7 L6 and L7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:20
L9	913	L5 xor L8 L5 and L8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:20
L10	978	L9 xor 2 L9 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:20

L11	41	706/902.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:21
L12	1015	10 xor 11 10 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:22
L13	1	12 and @ad>="20040824"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 09:23



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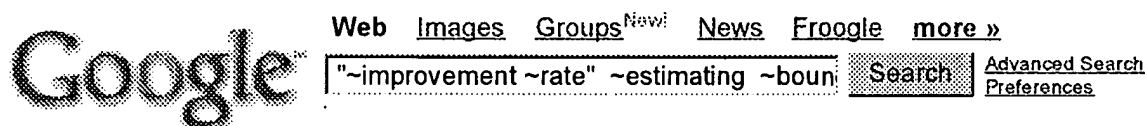
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... where: λ = the average **estimate** of the cumulative failure rate, fr/hr. ... the **improvement rate** in the , 0 1. ... Parameter **Estimation** for the Duane Model. ...

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... **rate** for drug is higher than the **improvement rate** for placebo ... a sam- ple of N2 allows **estimation** to within ... This approach will provide an **estimate** of the average ...

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... of this technique lies in **estimating** the accuracy ... **rate** of the discrepancy determines the **improvement rate** of the ... in accuracy of the resulting point **estimate**. ...

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... norms, it is still possible to **estimate** this cutoff ... The mean **improvement rate** at the follow-up evaluations was ... group support often exceeds the **bounds** of what ...

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Optimized SuperLink

... original greedy algorithm in order to **estimate** the problem ... sumTableImprovement is the **improvement rate** in the sum of ... to the pool until it reaches its **bounds**. ...

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... No data were found **estimating** the rate of minor ... Cure and **improvement rate** estimates were derived from the ... sensitivity analysis, we used an **estimate** of lower ...

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... difficult Memory performance **improvement rate** (7% per year) is far ... performance **bounds** (based on the three ... Page 11. **Estimating** the Memory Bandwidth Limitation ...

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... Although this is a poor **estimate** of accuracy (especially when computed from a ... three selection methods so that approximately 4 A fast **improvement rate** is not a ...

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... findings do not provide enough information to **estimate** long-run forage response ... alternative management strategies, or at the very least, provide **bounds** on the ...

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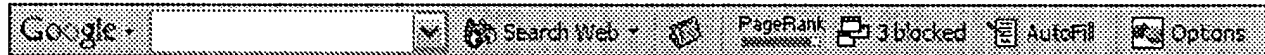
... rate less the 'relative technological **improvement rate**^{fl}, just as ... we may achieve a similar (under)**estimate** on K ... and we have appropriate uniform **bounds** on the ...

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1 [Classification: Categorizing information objects from user access patterns](#)

Mao Chen, Andrea LaPaugh, Jaswinder Pal Singh

 November 2002 **Proceedings of the eleventh international conference on Information and knowledge management**

 Full text available: pdf(321.09 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Many web sites have dynamic information objects whose topics change over time. Classifying these objects *automatically* and *promptly* is a challenging and important problem for site masters. Traditional content-based and link structure based classification techniques have intrinsic limitations for this task. This paper proposes a framework to classify an object into an existing category structure by analyzing the users' traversals in the category structure. The key idea is to infer a ...

Keywords: category structure, classification, dynamic object, multimedia, prediction, user accesses

2 [Responses to NIST's proposal](#)

Ronald L. Rivest, Martin E. Hellman, John C. Anderson, John W. Lyons

 July 1992 **Communications of the ACM**, Volume 35 Issue 7

 Full text available: pdf(3.06 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

3 [Distributed data sources: Efficient query routing in distributed spatial databases](#)

Roger Zimmermann, Wei-Shinn Ku, Wei-Cheng Chu

 November 2004 **Proceedings of the 12th annual ACM international workshop on Geographic information systems**

 Full text available: pdf(286.05 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Spatial databases are prominently used in Geographic Information System (GIS) applications. However, many of the current architectures rely on a centralized data repository. The next evolution will be GIS applications that utilize and integrate a multitude of remotely accessible data sets, for example via Web services. Our involvement in a project where geotechnical borehole information is retrieved from a large number of repositories that are under different administrative control has motivated ...

Keywords: database middleware, distributed spatial databases, query routing

4 Round robin classification

Johannes Fürnkranz

March 2002 **The Journal of Machine Learning Research**, Volume 2

Full text available:  pdf(250.25 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


In this paper, we discuss round robin classification (aka pairwise classification), a technique for handling multi-class problems with binary classifiers by learning one classifier for each pair of classes. We present an empirical evaluation of the method, implemented as a wrapper around the Ripper rule learning algorithm, on 20 multi-class datasets from the UCI database repository. Our results show that the technique is very likely to improve Ripper's classification accuracy without having a hi ...

Keywords: class binarization, ensemble techniques, inductive rule learning, multi-class problems, pairwise classification

5 Optimizing exact genetic linkage computations

Maayan Fishelson, Dan Geiger

April 2003 **Proceedings of the seventh annual international conference on Computational molecular biology**

Full text available:  pdf(188.29 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Genetic linkage analysis is a challenging application which requires Bayesian networks consisting of thousands of vertices. Consequently, computing the likelihood of data, which is needed for learning linkage parameters, using exact inference procedures calls for an extremely efficient implementation that carefully optimizes the order of conditioning and summation operations. In this paper we present the use of stochastic greedy algorithms for optimizing this order. Our algorithm has been incorp ...

Keywords: DAG models, bayesian networks, combinatorial optimization, genetic linkage analysis, greedy algorithms, probabilistic algorithms, superlink, treewidth

6 Energy efficient microarchitectural techniques: Pipeline stage unification: a low-energy consumption technique for future mobile processors

Hajime Shimada, Hideki Ando, Toshio Shimada

August 2003 **Proceedings of the 2003 international symposium on Low power electronics and design**

Full text available:  pdf(111.18 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Recent mobile processors are required to exhibit both low-energy consumption and high performance. To satisfy these requirements, dynamic voltage scaling (DVS) is currently employed. However, its effectiveness will be limited in the future because of shrinking the variable supply voltage range. As an alternative, we previously proposed pipeline stage unification (PSU), which unifies multiple pipeline stages without reducing the supply voltage at a power-saving mode. This paper compares effective ...


Keywords: dynamic voltage scaling, future process technology, low-power consumption, pipeline stage

7 Generating highly-routable sparse crossbars for PLDs

Guy Lemieux, Paul Leventis, David Lewis

February 2000 **Proceedings of the 2000 ACM/SIGDA eighth international symposium on**

Field programmable gate arrays

Full text available:  pdf(952.31 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A method for evaluating and constructing sparse crossbars which are both area efficient and highly routable is presented. The evaluation method uses a network flow algorithm to accurately compute the percentage of random test vectors that can be routed. The construction method attempts to maximize the spread of the switch locations, such that any given subset of input wires can connect to as many output wires as possible. Based on Hall's Theorem, we argue that this increases the likelihood ...

8 Session S4.2: program transformation: Optimizing inter-nest data locality

M. Kandemir, I. Kadayif, A. Choudhary, J. A. Zambreno

October 2002 **Proceedings of the 2002 international conference on Compilers, architecture, and synthesis for embedded systems**

Full text available:  pdf(272.47 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)





By examining data reuse patterns of four array-intensive embedded applications, we found that these codes exhibit a significant amount of inter-nest reuse (i. e., the data reuse that occurs between different nests). While traditional compiler techniques that target array-intensive applications can exploit intra-nest data reuse, there has not been much success in the past in taking advantage of inter-nest data reuse. In this paper, we present a compiler strategy that optimizes inter-nest reuse usi ...

Keywords: array-intensive codes, cache locality, data reuse, embedded applications, inter-nest optimization

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Keywords: dynamic voltage scaling, future process technology, low-power consumption, pipeline stage

4 [Software engineering environment: Software estimation using the SLIM tool](#)

Nikki Panlilio-Yap

November 1992 **Proceedings of the 1992 conference of the Centre for Advanced Studies on Collaborative research - Volume 1**


Full text available:  pdf(1.29 MB) Additional Information: [full citation](#), [abstract](#), [references](#)

Planning the development of a software product involves estimating the required effort, project duration, resource allocation, and latent defects when the product ships. Coming up with reasonable estimates seems to be a pervasive problem. In the absence of an internally developed tool based on historical data from the IBM Canada Ltd. Laboratory or similar IBM laboratories, the use of one or more commercially available estimation tools is useful. Some of these tools have underlying models based o ...

5 [Round robin classification](#)

Johannes Fürnkranz

March 2002 **The Journal of Machine Learning Research**, Volume 2

Full text available:  pdf(250.25 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


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Keywords: class binarization, ensemble techniques, inductive rule learning, multi-class problems, pairwise classification

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November 2002 **Proceedings of the eleventh international conference on Information and knowledge management**

Full text available:  pdf(321.09 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


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Keywords: category structure, classification, dynamic object, multimedia, prediction, user accesses

7 [The infeasibility of experimental quantification of life-critical software reliability](#)

Ricky W. Butler, George B. Finelli


September 1991 **ACM SIGSOFT Software Engineering Notes , Proceedings of the conference on Software for critical systems**, Volume 16 Issue 5

Full text available:  pdf(1.09 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

8 Analysis of Efficiency and Accuracy of Learning in Minority Games

Kiyoshi Izumi, Tomohisa Yamashita, Koichi Kurumatani

July 2004 **Proceedings of the Third International Joint Conference on Autonomous Agents and Multiagent Systems - Volume 3**

Full text available:  [pdf\(86.48 KB\)](#) Additional Information: [full citation](#), [abstract](#)

In this paper, we constructed three types of agents, which are different in efficiency and accuracy of learning. They were compared using acquired payoff in a game-theoretic situation, a minority game. As a result, different types of learning methods got the highest payoff according to the complexity of environmental change and learning speed.

9 Reconfigurable computing: analysis and trends: FPGAs vs. CPUs: trends in peak floating-point performance

Keith Underwood

February 2004 **Proceeding of the 2004 ACM/SIGDA 12th international symposium on Field programmable gate arrays**

Full text available:  [pdf\(206.40 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


Moore's Law states that the number of transistors on a device doubles every two years; however, it is often (mis)quoted based on its impact on CPU performance. This important corollary of Moore's Law states that improved clock frequency plus improved architecture yields a doubling of CPU performance every 18 months. This paper examines the impact of Moore's Law on the peak floating-point performance of FPGAs. Performance trends for individual operations are analyzed as well as the performance tr ...

Keywords: FPGA, floating point, supercomputing, trends

10 Using background knowledge to speed reinforcement learning in physical agents

Daniel Shapiro, Pat Langley, Ross Shachter

May 2001 **Proceedings of the fifth international conference on Autonomous agents**

Full text available:  [pdf\(483.84 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


This paper describes Icarus, an agent architecture that embeds a hierarchical reinforcement learning algorithm within a language for specifying agent behavior. An Icarus program expresses an approximately correct theory about how to behave with options at varying levels of detail, while the Icarus agent determines the best options by learning from experience. We describe Icarus and its learning algorithm, then report on two experiments in a vehicle control domain. The first examines the ben ...

Keywords: action selection and planning, adaptation and learning, agent architectures, hierarchical reinforcement learning

11 Distributed data sources: Efficient query routing in distributed spatial databases

Roger Zimmermann, Wei-Shinn Ku, Wei-Cheng Chu

November 2004 **Proceedings of the 12th annual ACM international workshop on Geographic information systems**



Full text available:  [pdf\(286.06 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Spatial databases are prominently used in Geographic Information System (GIS) applications. However, many of the current architectures rely on a centralized data repository. The next evolution will be GIS applications that utilize and integrate a multitude of remotely accessible data sets, for example via Web services. Our involvement in a project where geotechnical borehole information is retrieved from a large number of

repositories that are under different administrative control has motiva ...

Keywords: database middleware, distributed spatial databases, query routing

- 12 Performance characterization of a Quad Pentium Pro SMP using OLTP workloads
Kimberly Keeton, David A. Patterson, Yong Qiang He, Roger C. Raphael, Walter E. Baker
April 1998 **ACM SIGARCH Computer Architecture News , Proceedings of the 25th annual international symposium on Computer architecture**, Volume 26 Issue 3

Full text available:  [pdf\(1.58 MB\)](#)  Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
[Publisher Site](#)


Commercial applications are an important, yet often overlooked, workload with significantly different characteristics from technical workloads. The potential impact of these differences is that computers optimized for technical workloads may not provide good performance for commercial applications, and these applications may not fully exploit advances in processor design. To evaluate these issues, we use hardware counters to measure architectural features of a four-processor Pentium Pro-based se ...

- 13 A first step to formally evaluate collaborative work
Ricardo Baeza-Yates, José A. Pino
November 1997 **Proceedings of the international ACM SIGGROUP conference on Supporting group work : the integration challenge: the integration challenge**

Full text available:  [pdf\(725.59 KB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: CSCW, collaborative retrieval, quantitative evaluation

- 14 A case for Wafer-scale interconnected memory arrays
T.-C Chiueh
December 1992 **Proceedings of the 1992 ACM/IEEE conference on Supercomputing**

Full text available:  [pdf\(865.24 KB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)

- 15 Session S4.2: program transformation: Optimizing inter-ness data locality
M. Kandemir, I. Kadayif, A. Choudhary, J. A. Zambreno
October 2002 **Proceedings of the 2002 international conference on Compilers, architecture, and synthesis for embedded systems**

Full text available:  [pdf\(272.47 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

By examining data reuse patterns of four array-intensive embedded applications, we found that these codes exhibit a significant amount of inter-ness reuse (i. e., the data reuse that occurs between different nests). While traditional compiler techniques that target array-intensive applications can exploit intra-ness data reuse, there has not been much success in the past in taking advantage of interness data reuse. In this paper, we present a compiler strategy that optimizes inter-ness reuse usi ...

Keywords: array-intensive codes, cache locality, data reuse, embedded applications, inter-ness optimization

- 16 Generating highly-routable sparse crossbars for PLDs

Guy Lemieux, Paul Leventis, David Lewis

February 2000 **Proceedings of the 2000 ACM/SIGDA eighth international symposium on Field programmable gate arrays**

Full text available:  pdf(952.31 KB)


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A method for evaluating and constructing sparse crossbars which are both area efficient and highly routable is presented. The evaluation method uses a network flow algorithm to accurately compute the percentage of random test vectors that can be routed. The construction method attempts to maximize the spread of the switch locations, such that any given subset of input wires can connect to as many output wires as possible. Based on Hall's Theorem, we argue that this increases the likelihood ...

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Kazutaka Murakami, Hyong S. Kim

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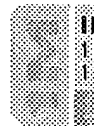
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2 Convergence behavior of affine projection algorithms
Sankaran, S.G.; Beex, A.A.L.;

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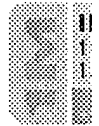
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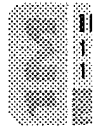
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Circuits and Systems for Video Technology, IEEE Transactions on , Volume: 12 , Issue: 5 , May 2002

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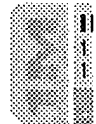
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